

# Vishay Semiconductors

# **Small Signal Schottky Diode**



## **DESIGN SUPPORT TOOLS** click logo to get started



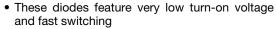
### **MECHANICAL DATA**

Case: SOD-323

Weight: approx. 4.3 mg
Packaging codes/options:

18/10K per 13" reel (8 mm tape), 10K/box 08/3K per 7" reel (8 mm tape), 15K/box

#### **FEATURES**





 These devices are protected by a PN junction guard ring against excessive voltage, such as electrostatic discharges



AEC-Q101 qualified available

Base P/N-E3 - RoHS-compliant, commercial grade

Base P/N-HE3 - RoHS-compliant, AEC-Q101 qualified

 Material categorization: for definitions of compliance please see <a href="https://www.vishay.com/doc?99912"><u>www.vishay.com/doc?99912</u></a>

PARTS TABLE					
PART	ORDERING CODE	CIRCUIT CONFIGURATION	TYPE MARKING	REMARKS	
BAT54WS	BAT54WS-E3-08 or BAT54WS-E3-18	Single	L4	Tape and reel	
	BAT54WS-HE3-08 or BAT54WS-HE3-18	Single	L <del>4</del>		

ABSOLUTE MAXIMUM RATINGS (T <sub>amb</sub> = 25 °C, unless otherwise specified)				
PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT
Repetitive peak reverse voltage		$V_{RRM}$	30	V
Forward continuous current (1)		I <sub>F</sub>	200	mA
Repetitive peak forward current (1)		I <sub>FRM</sub>	300	mA
Surge forward current (1)	t <sub>p</sub> < 1 s	I <sub>FSM</sub>	600	mA
Power dissipation (1)		P <sub>tot</sub>	150	mW

#### Note

(1) Valid provided that electrodes are kept at ambient temperature

	•					
THERMAL CHARACTERISTICS (T <sub>amb</sub> = 25 °C, unless otherwise specified)						
PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT		
Thermal resistance junction to ambient air (1)		R <sub>thJA</sub>	650	K/W		
Maximum junction temperature		T <sub>j</sub>	125	°C		
Storage temperature range		T <sub>stg</sub>	-65 to +150	°C		
Operating temperature range		Top	-55 to +125	°C		

#### Note

(1) Valid provided that electrodes are kept at ambient temperature

<b>ELECTRICAL CHARACTERISTICS</b> (T <sub>amb</sub> = 25 °C, unless otherwise specified)						
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT
Reverse breakdown voltage	Tested with 100 µA pulses	V <sub>(BR)</sub>	30			V
Leakage current (1)	V <sub>R</sub> = 25 V	I <sub>R</sub>			2	μΑ
	$I_F = 0.1 \text{ mA}$	V <sub>F</sub>			240	mV
	I <sub>F</sub> = 1 mA	V <sub>F</sub>			320	mV
Forward voltage (1)	$I_F = 10 \text{ mA}$	V <sub>F</sub>			400	mV
	$I_F = 30 \text{ mA}$	V <sub>F</sub>			500	mV
	$I_F = 100 \text{ mA}$	$V_{F}$			800	mV
Diode capacitance	$V_R = 1 V, f = 1 MHz$	C <sub>D</sub>			10	pF
Reserve recovery time	$I_F$ = 10 mA, $I_R$ = 10 mA, $I_R$ = 1 mA, $R_L$ = 100 $\Omega$	t <sub>rr</sub>			5	ns

#### Note

(1) Pulse test;  $t_p < 300 \mu s$ ,  $\theta < 2 \%$ 



## TYPICAL CHARACTERISTICS (T<sub>amb</sub> = 25 °C, unless otherwise specified)

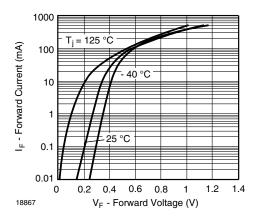


Fig. 1 - Typical Forward Current vs. Forward Voltage vs. Various Temperatures

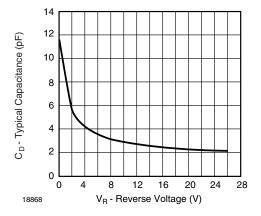


Fig. 2 - Typical Capacitance vs. Reverse Applied Voltage

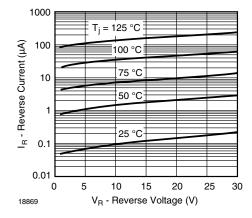


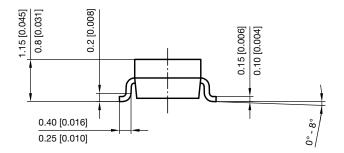
Fig. 3 - Typical Reverse Current vs. Reverse Voltage vs. Various Temperatures

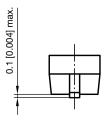


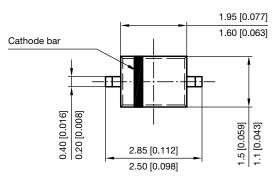
### www.vishay.com

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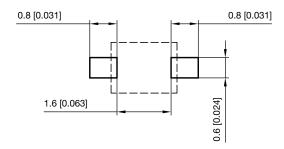
## PACKAGE DIMENSIONS in millimeters (inches): SOD-323







#### Footprint recommendation:



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